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PATENT
81788.0163

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE **RECEIVED**

In re application of:

Yoshiyuki Tanaka, et al.

Serial No: 09/130,818

Filed: August 7, 1998

For: METHOD FOR CONTROLLING NON-VOLATILE SEMICONDUCTOR MEMORY SYSTEM BY USING LOOK UP TABLE

Art Unit: 2186

MAR 12 2004

Confirmation No.: 6857 Technology Center 2100

Examiner: David L. Robertson

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

March 3, 2004

Date of Deposit

William H. Wright, Reg. No. 36,312

Name

3/3/04

Date

Signature

PRELIMINARY AMENDMENT

MAIL STOP RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Before examining the present application, please enter the following amendments:

Amendments to the specification are discussed on page 2 of this paper.

Amendments to the claims are reflected in the listing of claims, which begins on page 3 of this paper.

The filing of formal drawings is discussed on page 4 of this paper.

Remarks/Arguments begin on page 5 of this paper.